

REMARKS/ARGUMENTS

Favorable reconsideration of this application as presently amended and in light of the following remarks is respectfully requested.

Claims 1, 2, 8, 9, 12 and 13 are presently active in this application; Claims 1 and 2 having been amended by the present amendment.

In the outstanding Office Action Claims 1, 2, 8, 9, 12 and 13 were rejected under 35 USC 101 as claiming the same invention as that of Claims 1-6 of U.S. Patent No. 6,714,604 B1.

In response to the outstanding double patenting rejection, Claims 1 and 2 have been amended to delete reference to “in batch” with regard to the functionality of the recited “frequency converter”. In contrast, Claims 1-6 of the cited U.S. Patent No. 6,714,604 B1 each define the functionality of the frequency converter and of the analog orthogonal demodulator recited in Claims 1-6 in terms of “in batch” signal processing. None of the pending claims recite “in batch” functionality. In view of this distinction, it is respectfully submitted that the pending claims clearly do not recite the “same” invention as recited in the ‘604 patent and that the outstanding rejection has been overcome.

Furthermore, to expedite issuance of a patent from the present application and to avoid any possible further delay in issuance due to “obviousness” double patenting concerns, submitted herewith is a duly executed Terminal Disclaimer.

Accordingly, no further issues are outstanding, and the present application is believed

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to be in condition for formal allowance. An early and favorable action to that effect is respectfully respected.

Respectfully submitted,

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